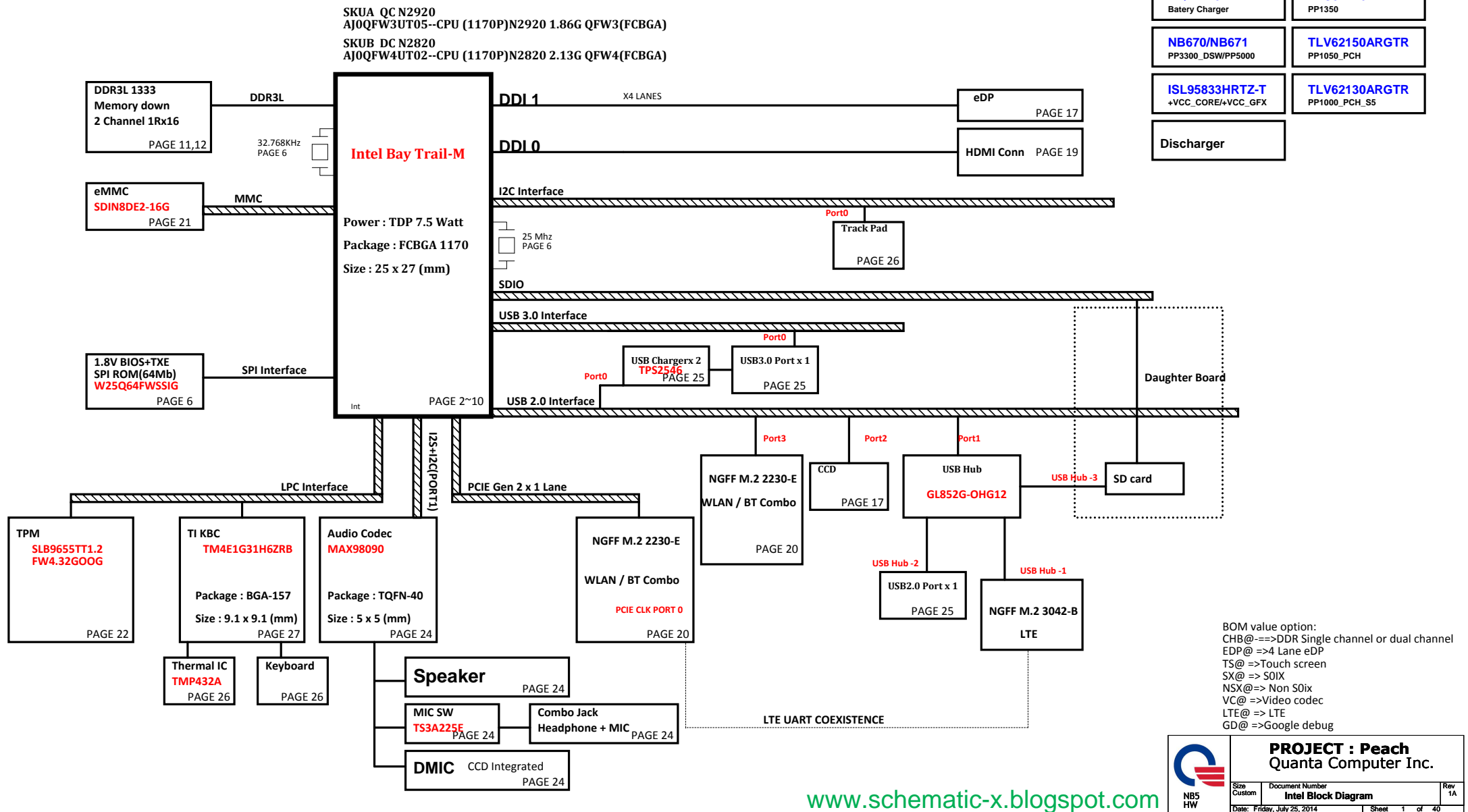
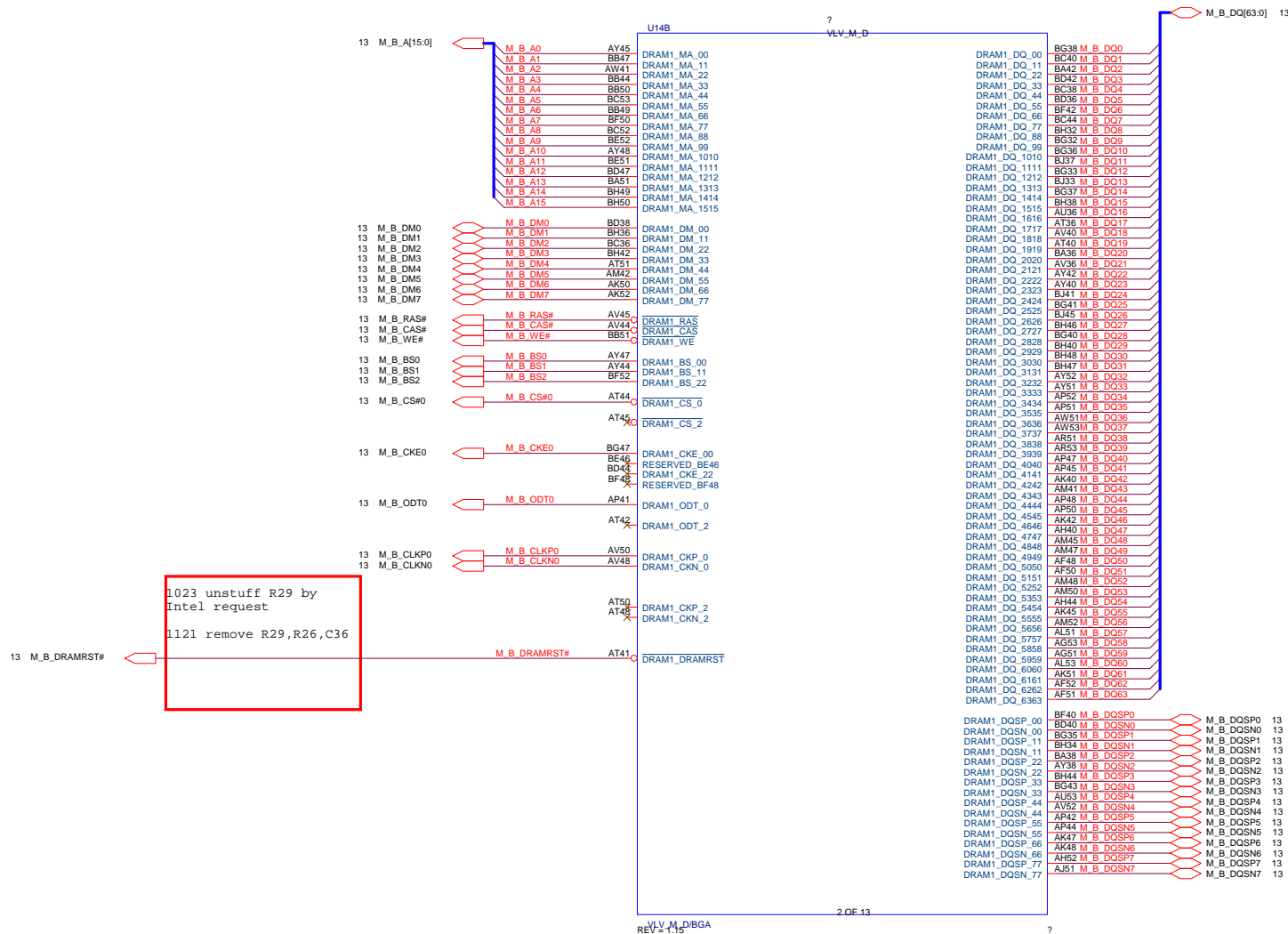
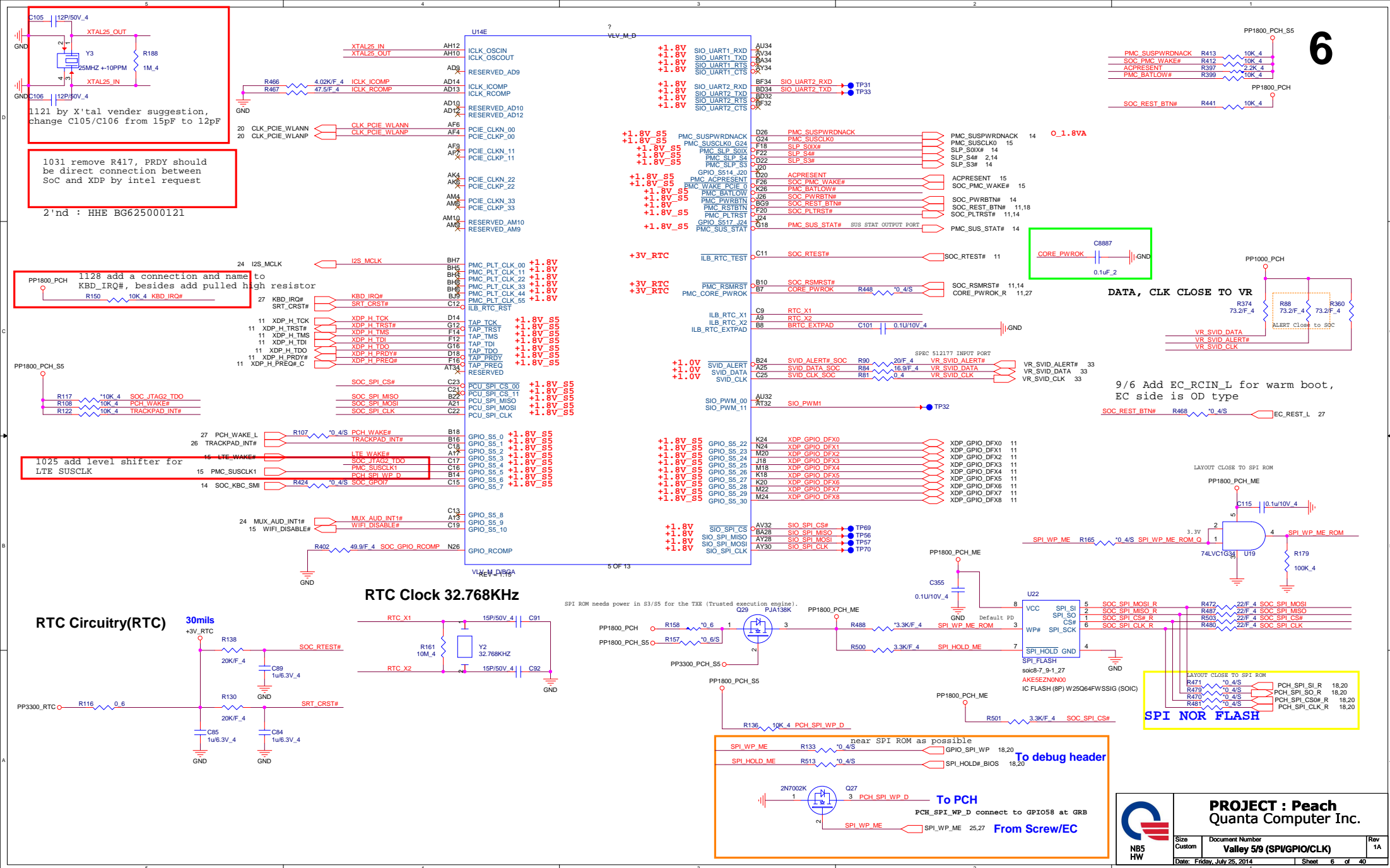
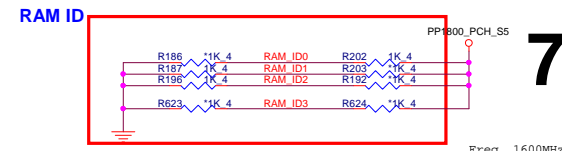


Intel Bay Trail-M Platform Block Diagram



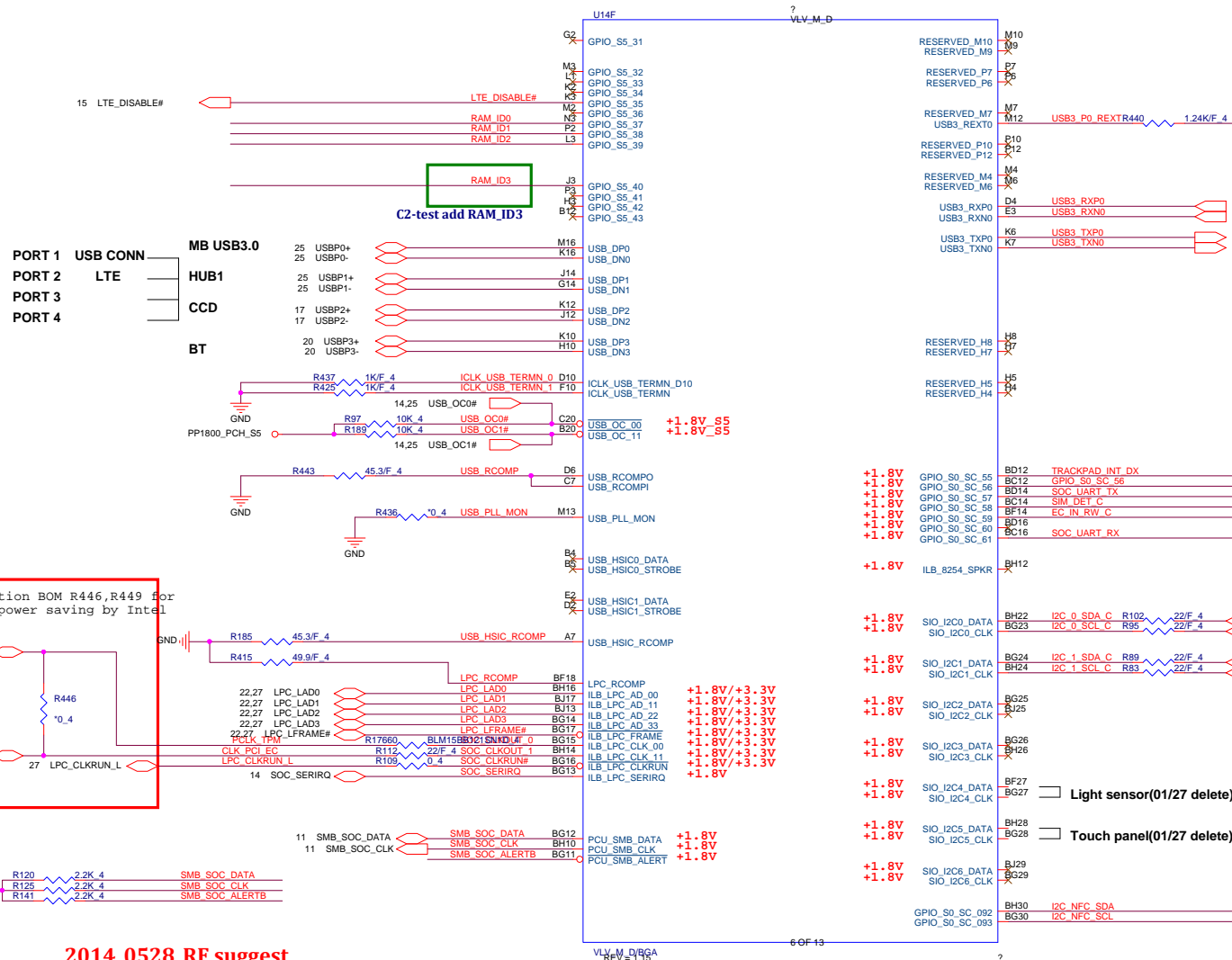




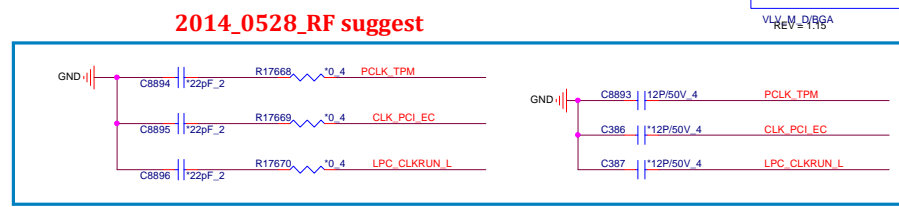
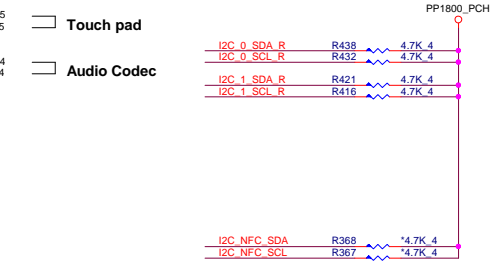
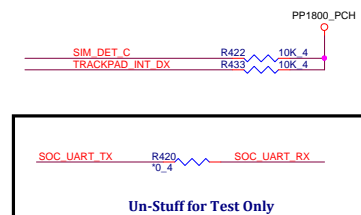


Vender	RAM_ID	Q PN	Mfr. PN	Channel	Size
Hynix	001	AKD5JGFTW11	H5TC4G63AFR-PBA	2CH	4GB
Elpida	000	AKD5JGST410	EDJ4216EFBG-GNL-F	2CH	4GB
Hynix	011	AKD5JGFTW14	H5TC4G63AFR-PBR	2CH	4GB
Micron	010	AKD5DGSTL07	MT41K128M16JT-125M:K	2CH	2GB
Elpida	100	AKD5JGST410	EDJ4216EFBG-GNL-F	1CH	2GB
Hynix	101	AKD5JGFTW11	H5TC4G63AFR-PBA	1CH	2GB
Hynix	110	AKD5JGFTW14	H5TC4G63AFR-PBR	1CH	2GB

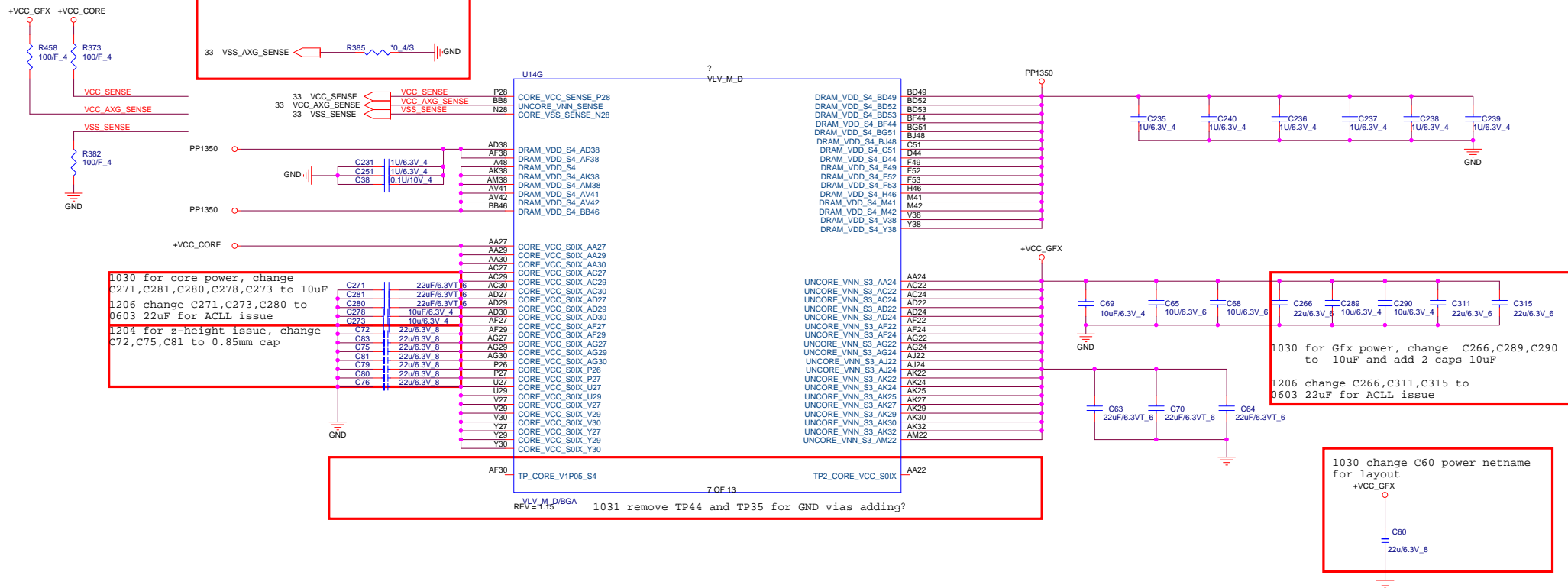
C-test sku2
C-test sku4
C-test sku3
C-test sku1



1101 add option BOM R446,R449 for EC CLK for power saving by Intel request

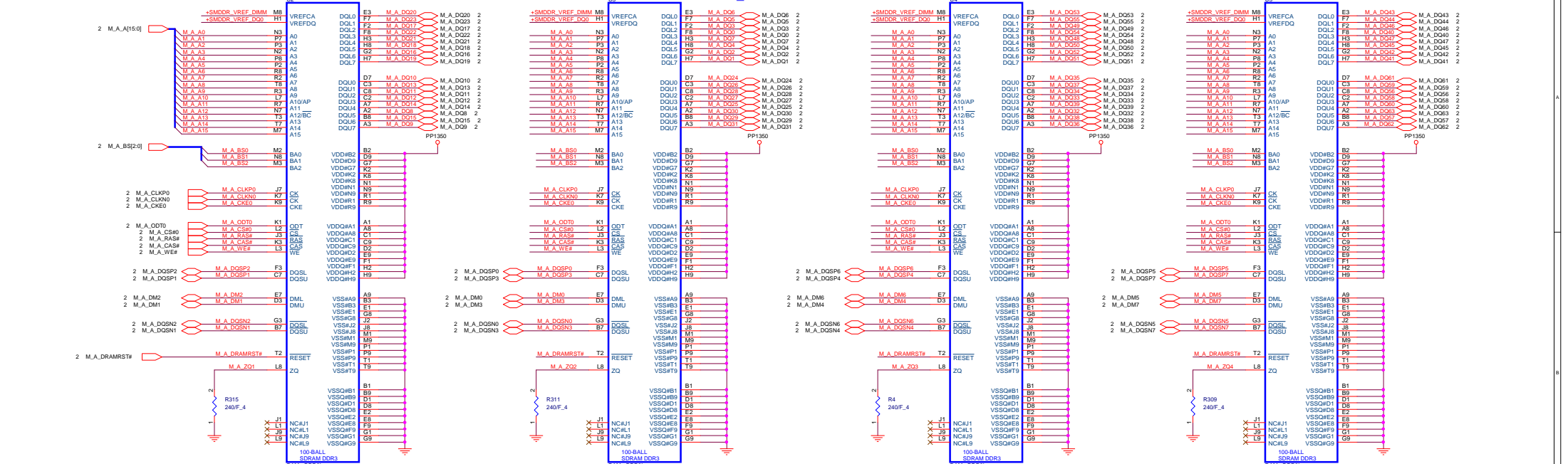


1031 for layout suggestion by intel, VSS_AXG_SENSE didn't connect to VSS_SENSE, will connect the GND via near VCC_AXG_SENSE
1031 for layout, add 0hm between GND and VSS_AXG_SENSE

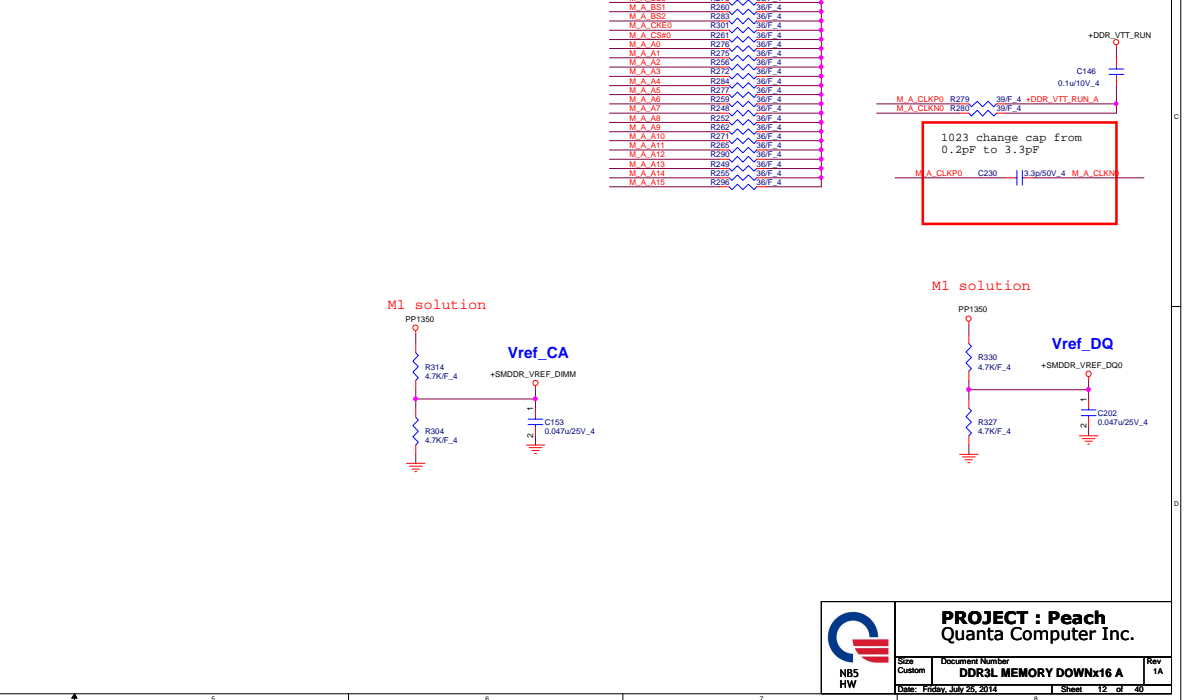
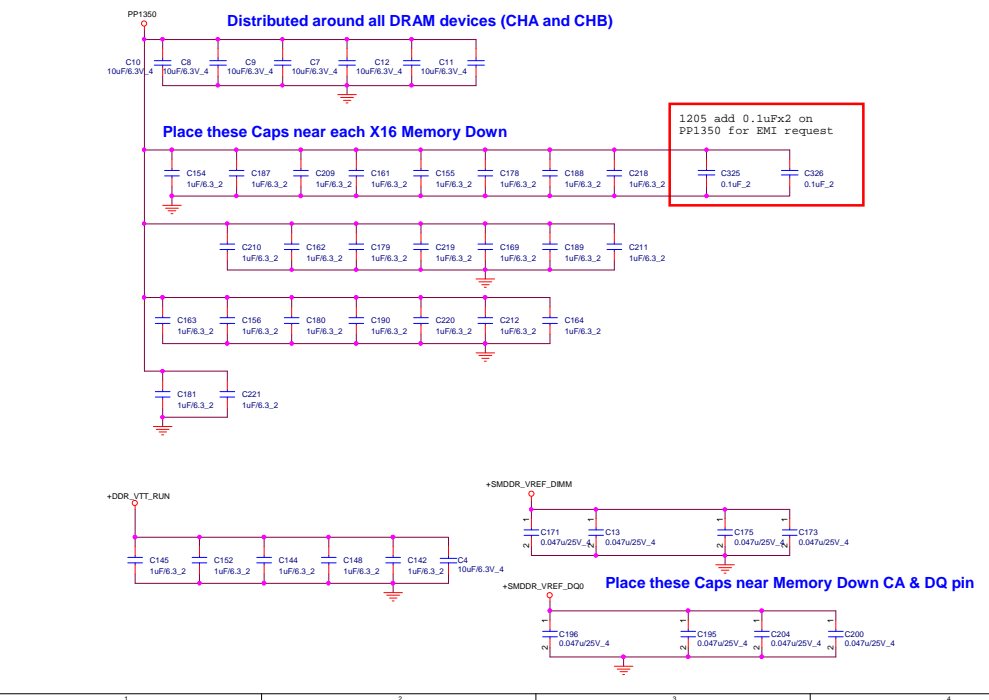


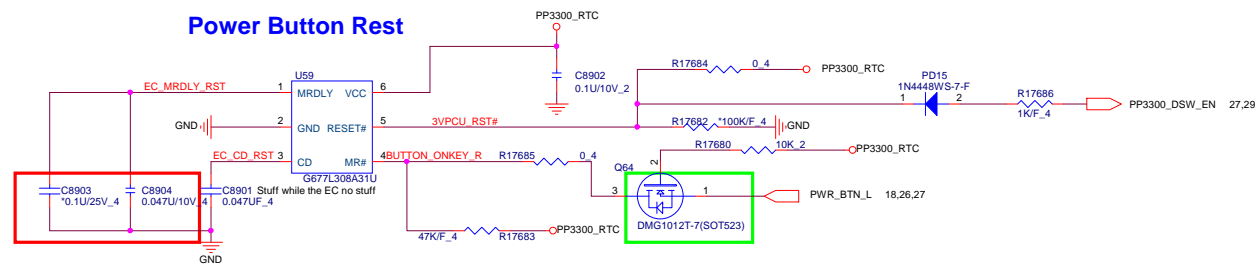
PROJECT : Peach
Quanta Computer Inc.

Size Custom	Document Number Valley 7/9 (Power 1)	Rev 1A
Date: Friday, July 25, 2014	Sheet 8 of 40	



Vendor	P/N
Hynix	AKD5JGETW00-H5TC4G63AFR-PBA
Elpida	AKD5JGST404 DDR3L 1333MHz 4Gb
	AKD5JGST404 DDR3L 1600MHz 4Gb





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Size Custom	Document Number PM Board/Reset PBT	Rev 1A
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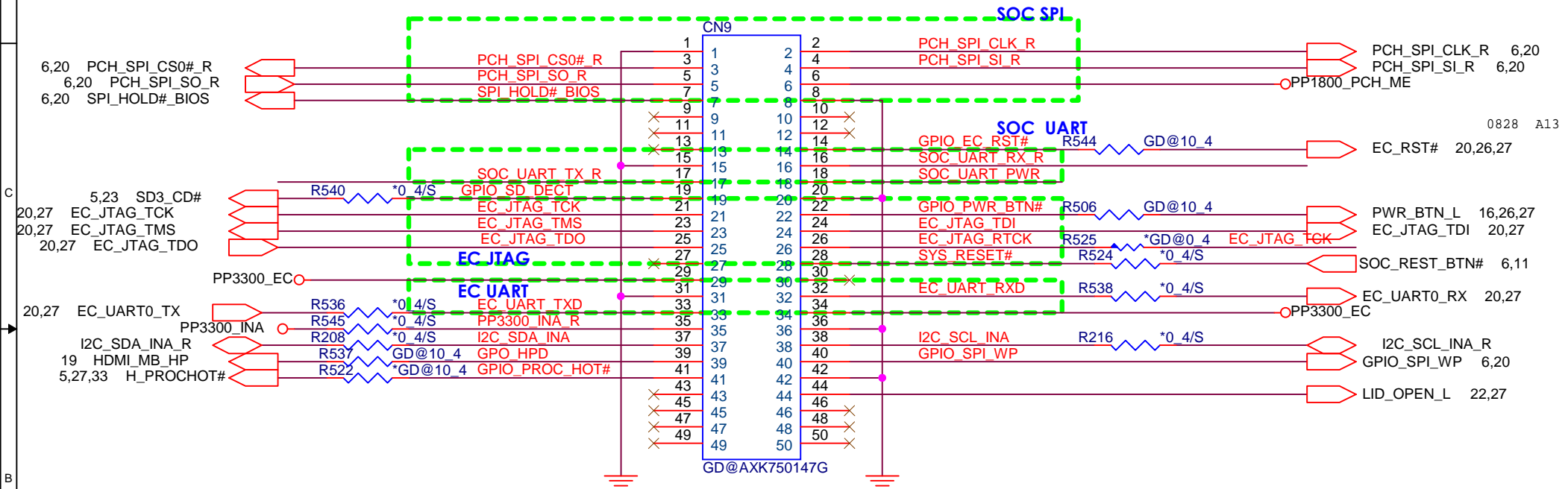
GOOGLE Debug Port(MPC)

50 pin BTB is MUST, don't use 42 pin

Socket part number AXK750147G

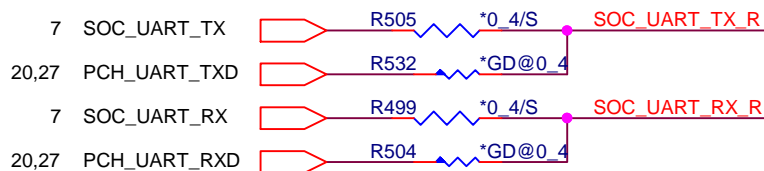
PIN7 OD	PIN39 OD	PIN49 OD
PIN14 OD	PIN41 OD	PIN50 OD
PIN19 OD	PIN43 OD	
PIN22 OD	PIN44 OD	
PIN28 OD	PIN45 OD	
PIN30 OD	PIN46 OD	
PIN37 OD	PIN47 OD	
PIN38 OD	PIN48 OD	

18



1021 change footprint and PN

9/13 add pull up



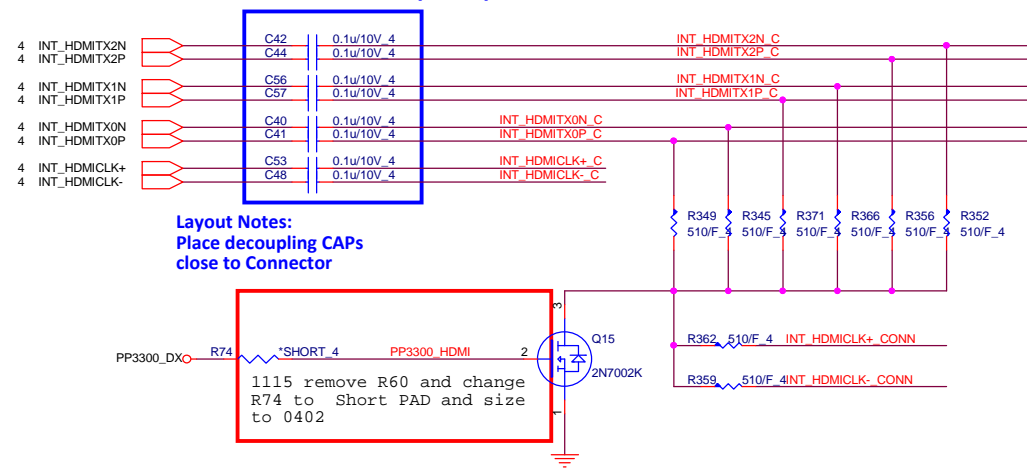
9/6 using optional instead of level shifted, default is from SoC



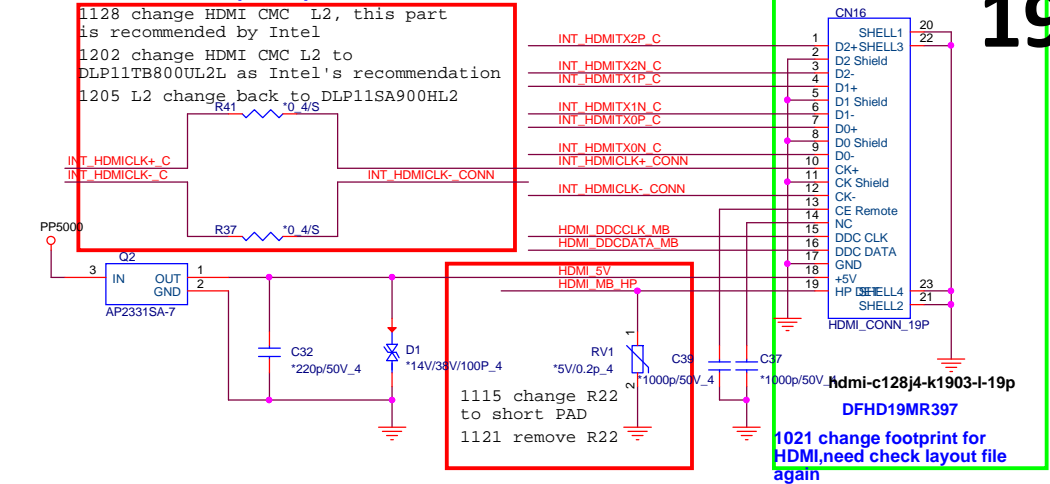
PROJECT : Peach
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HDMI Cost Reduced level shift (HDM)

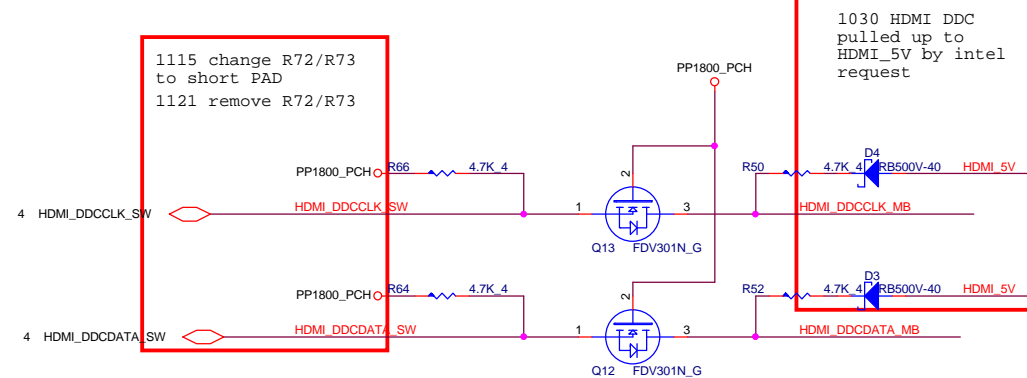


HDMI connector (HDM)

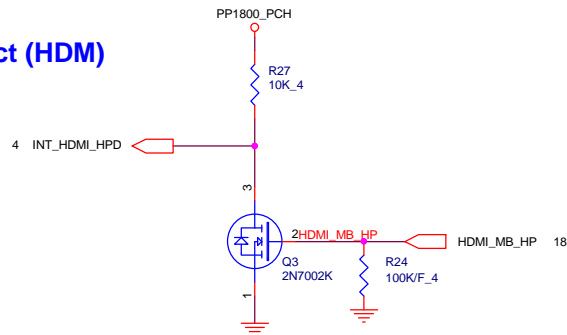


19

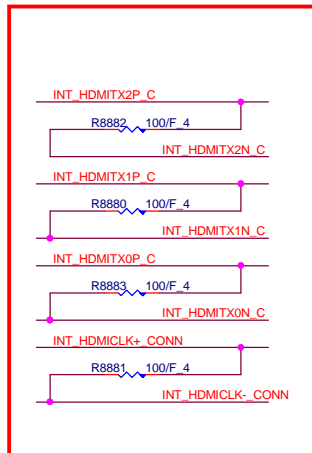
HDMI DDC (HDM)



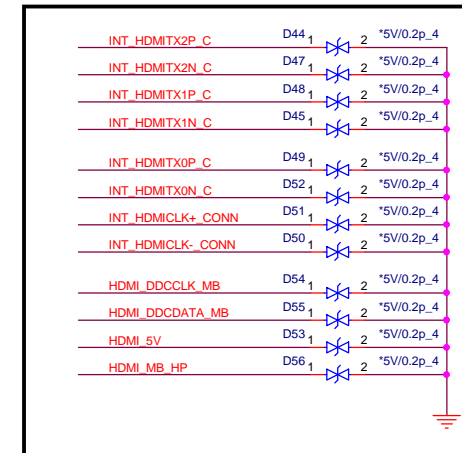
HDMI-detect (HDM)



EMI

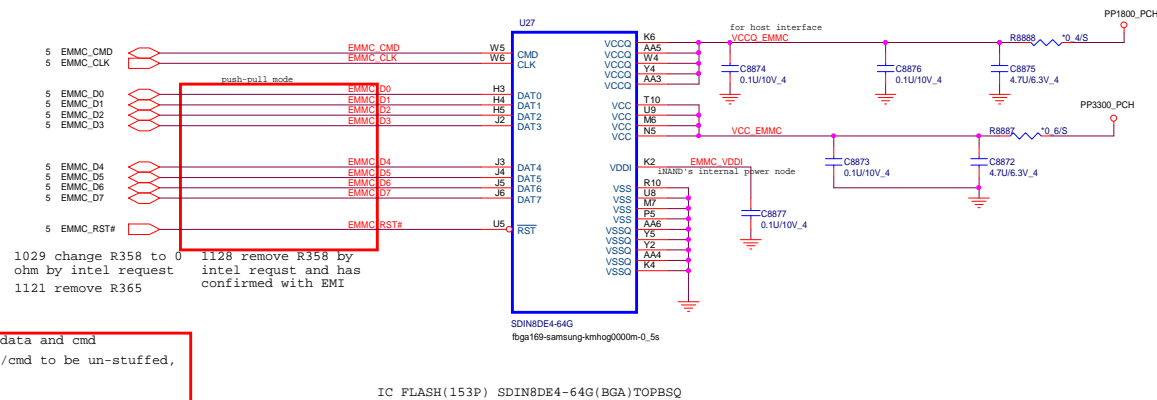


ESD 靠近HDMI CONNECTOR(CN16)

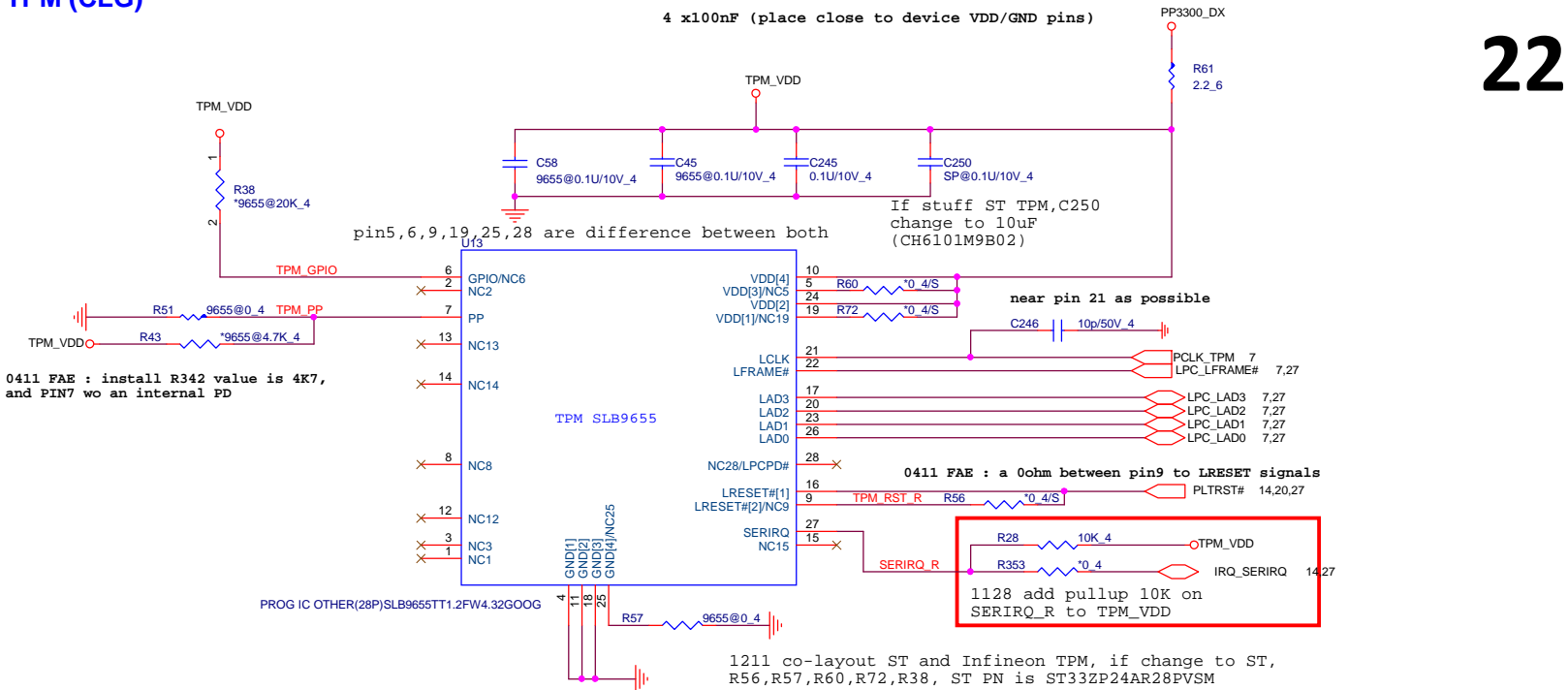


1025 Delete complete SSD(connector and caps)

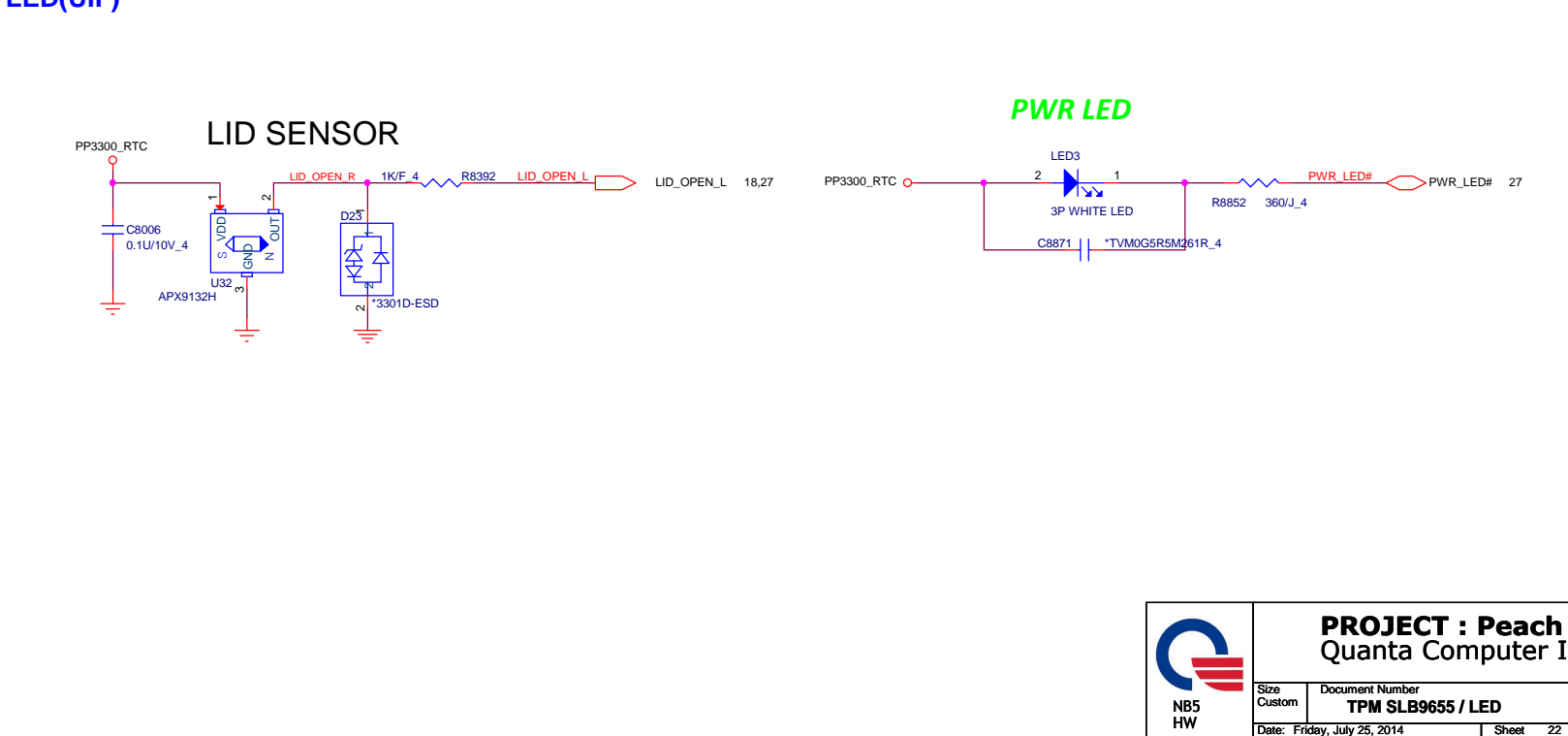
EMMC (CBS)



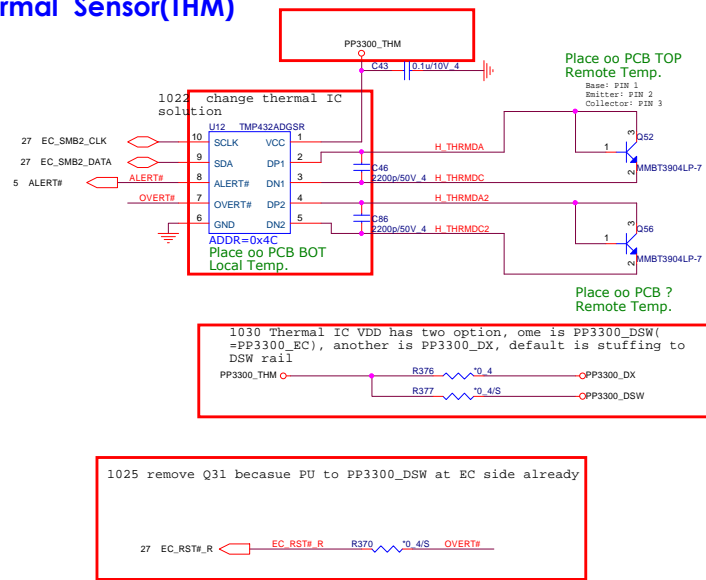
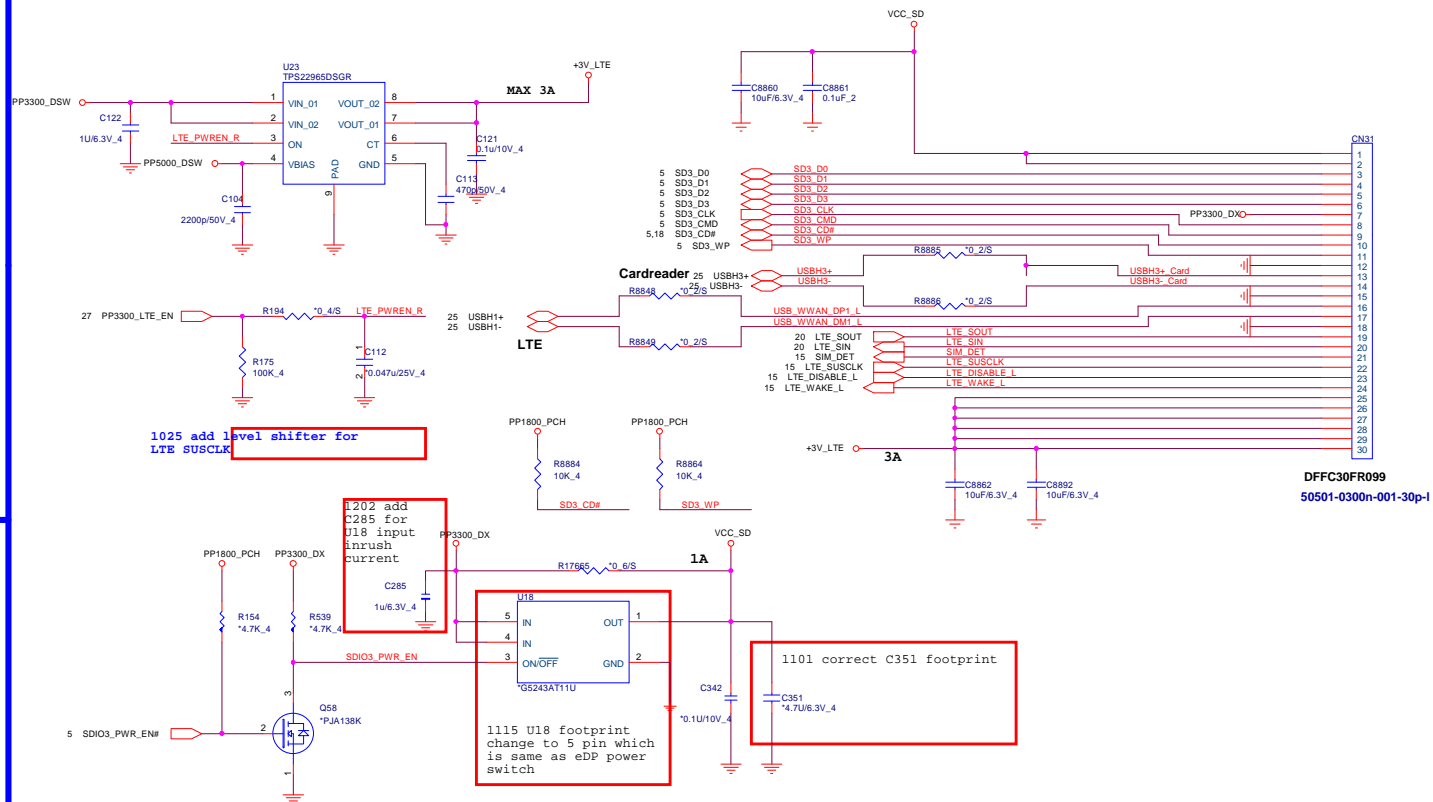
TPM (CLG)



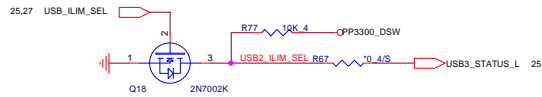
LED(UIF)

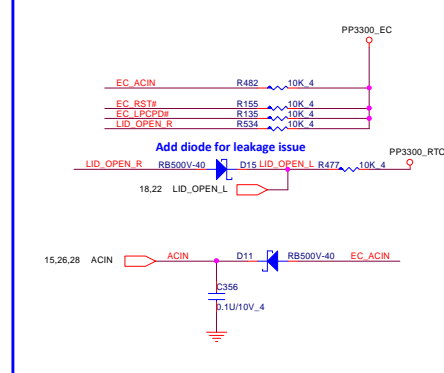
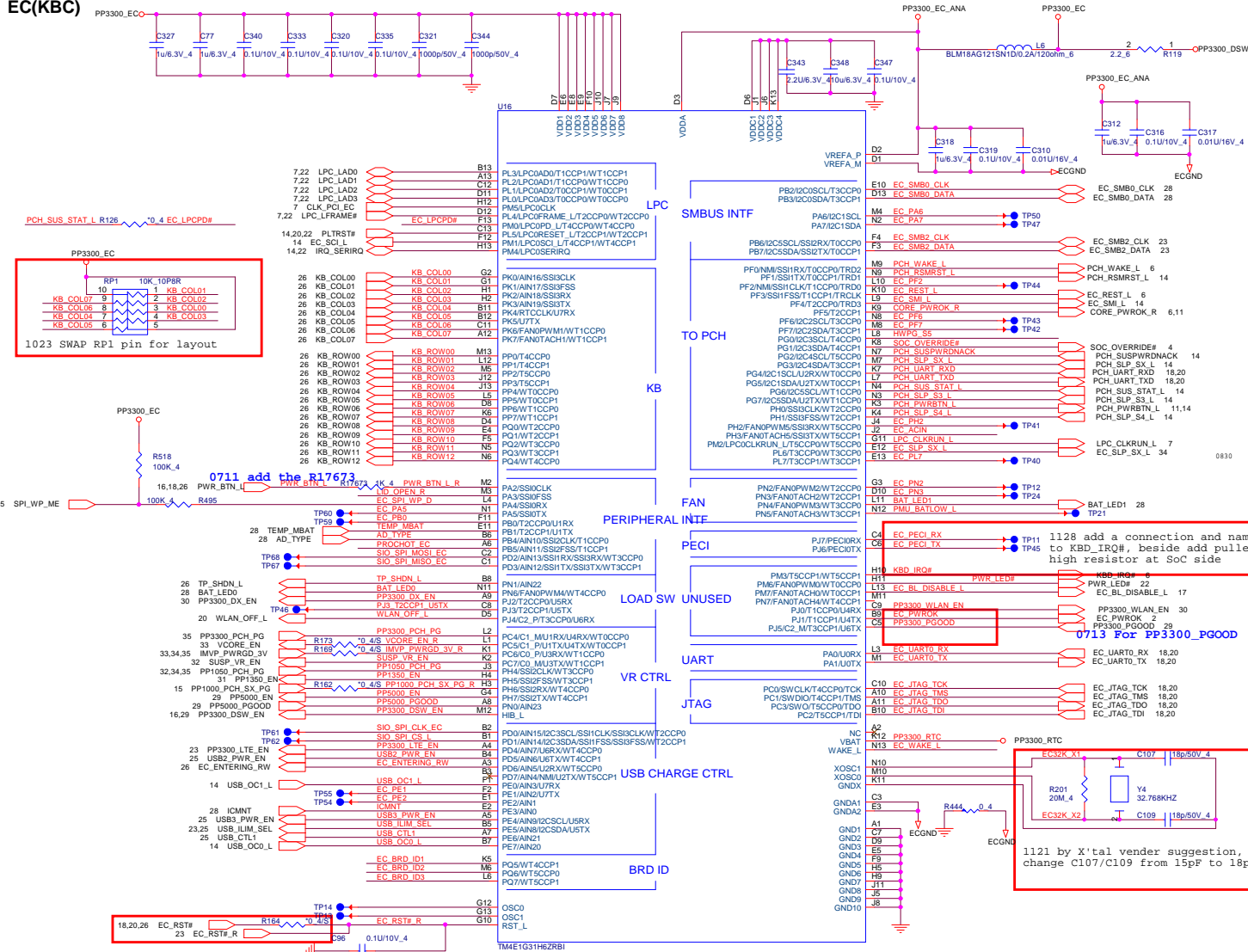


Thermal Sensor(THM)

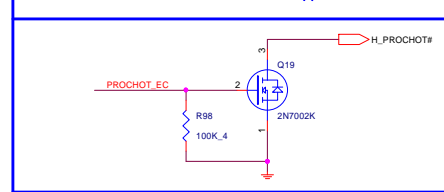
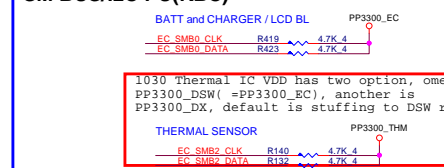
FUNCTION DB
LTE(MNC)

USB Switch Current Control

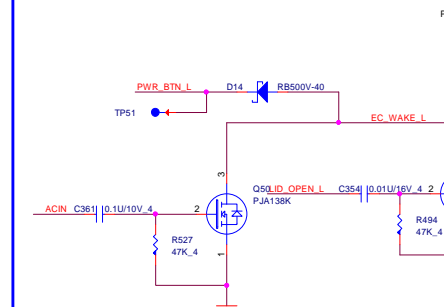




SM BUS/I2C PU(KBC)



EC HIB WAKE SOURCES



SM BUS ARRANGEMENT TABLE

SM Bus 0	BATT and CHARGER
SM Bus 1	NA
SM Bus 2	THERMAL SENSOR

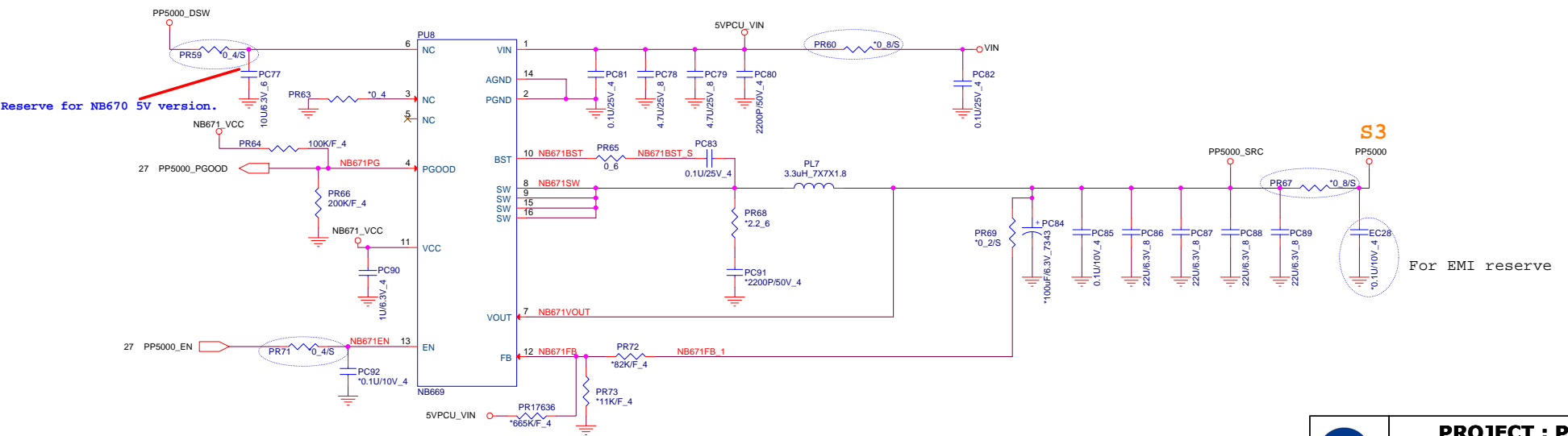
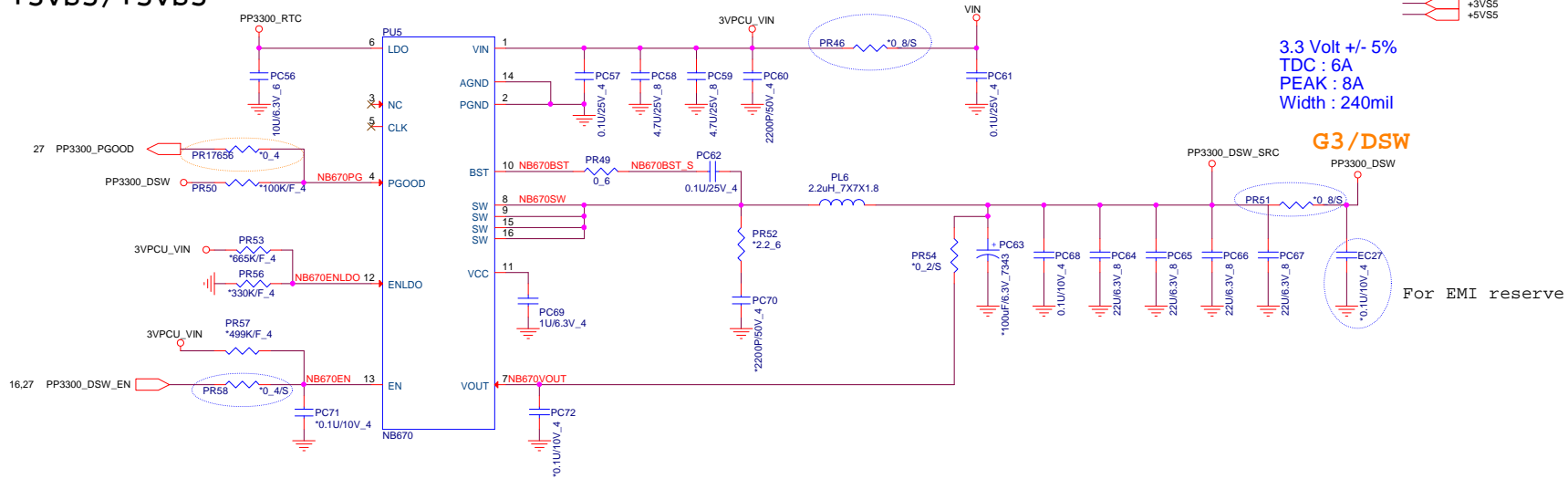
OD pin list

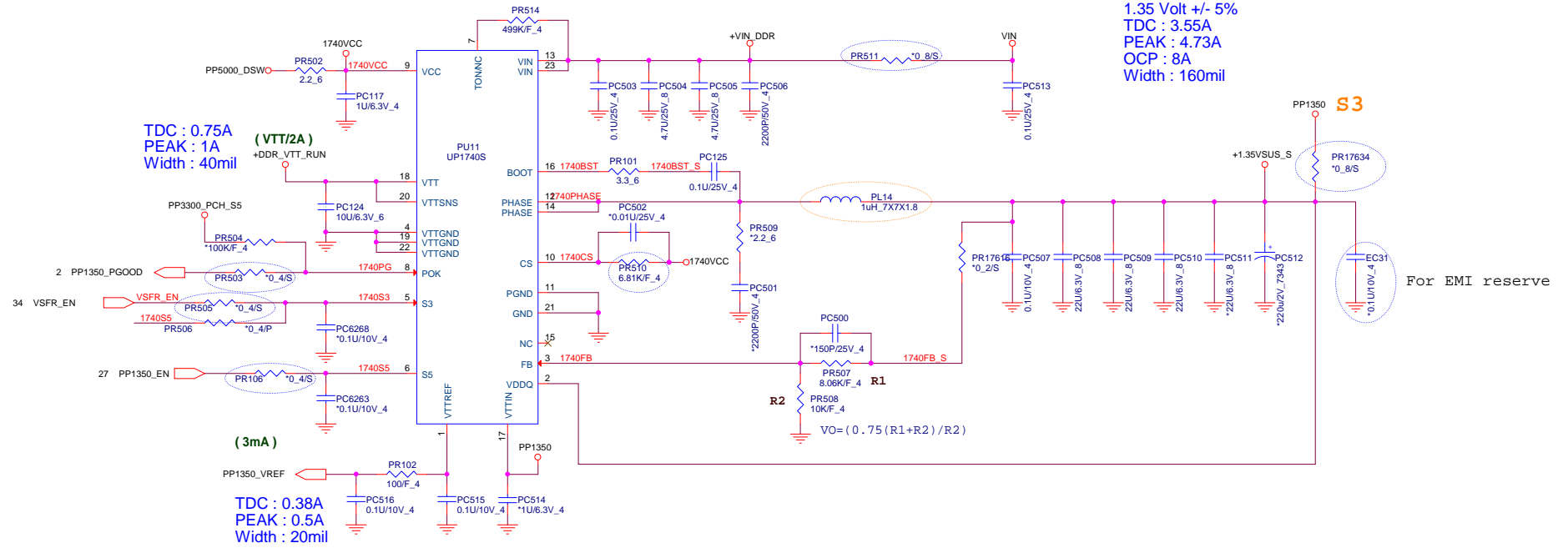
EC_RST_L
BAT_LED0
BAT_LED1
PCH_RSMRST_L
SMBUS
IRQ_SERIRQ
EC_BL_DISABLE_L

For testing only 0714 delete Power BTN

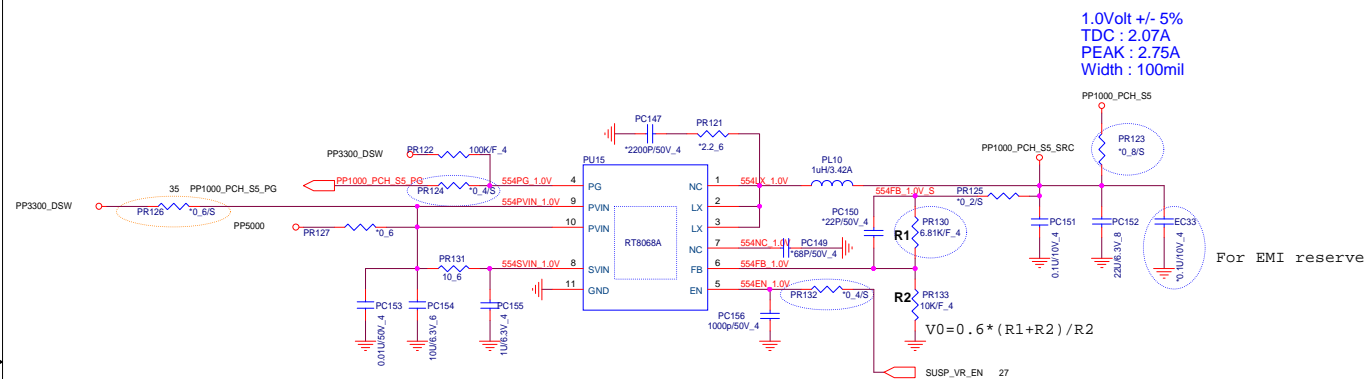
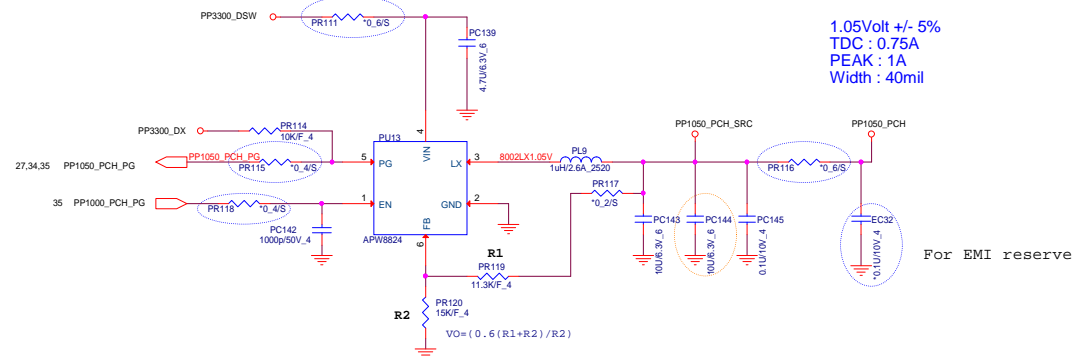


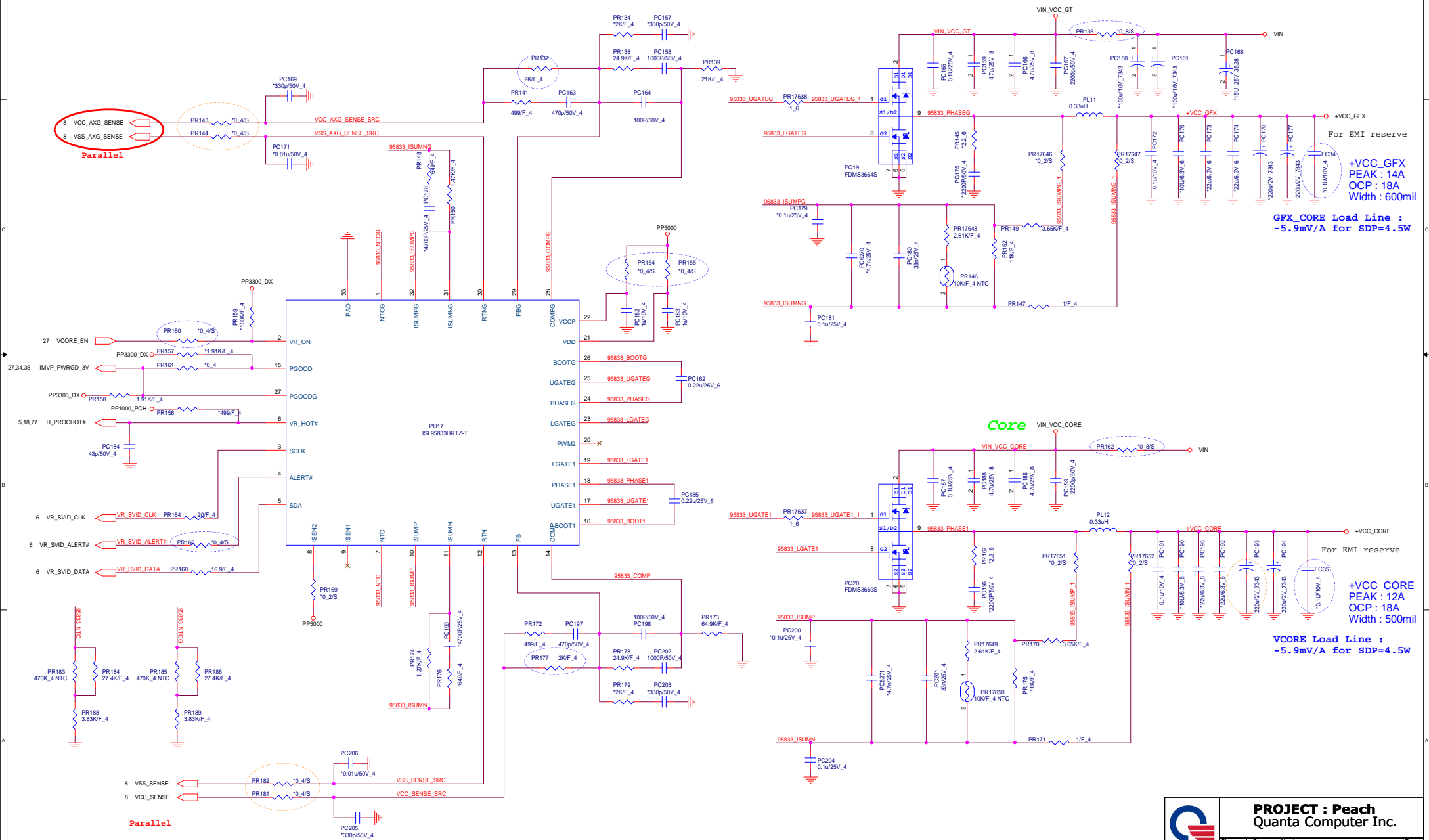
DC/DC +3VS5/+5VS5

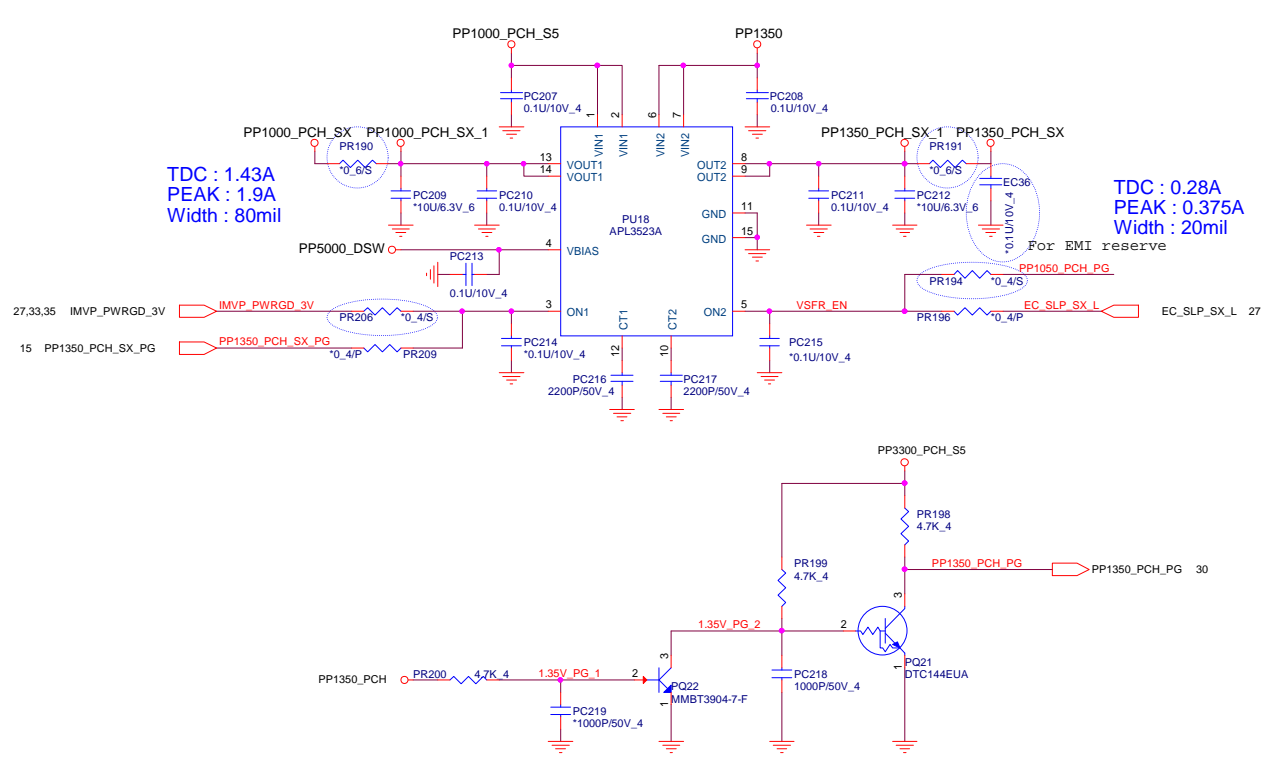


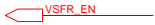




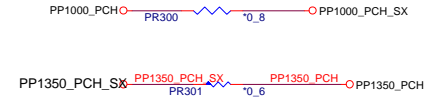
	S3	S5	+1.35VSUS	REF	VTT
S0	1	1	ON	ON	ON
S3 (main on off)	0	1	ON	ON	OFF
S4/S5	0	0	OFF	OFF	OFF

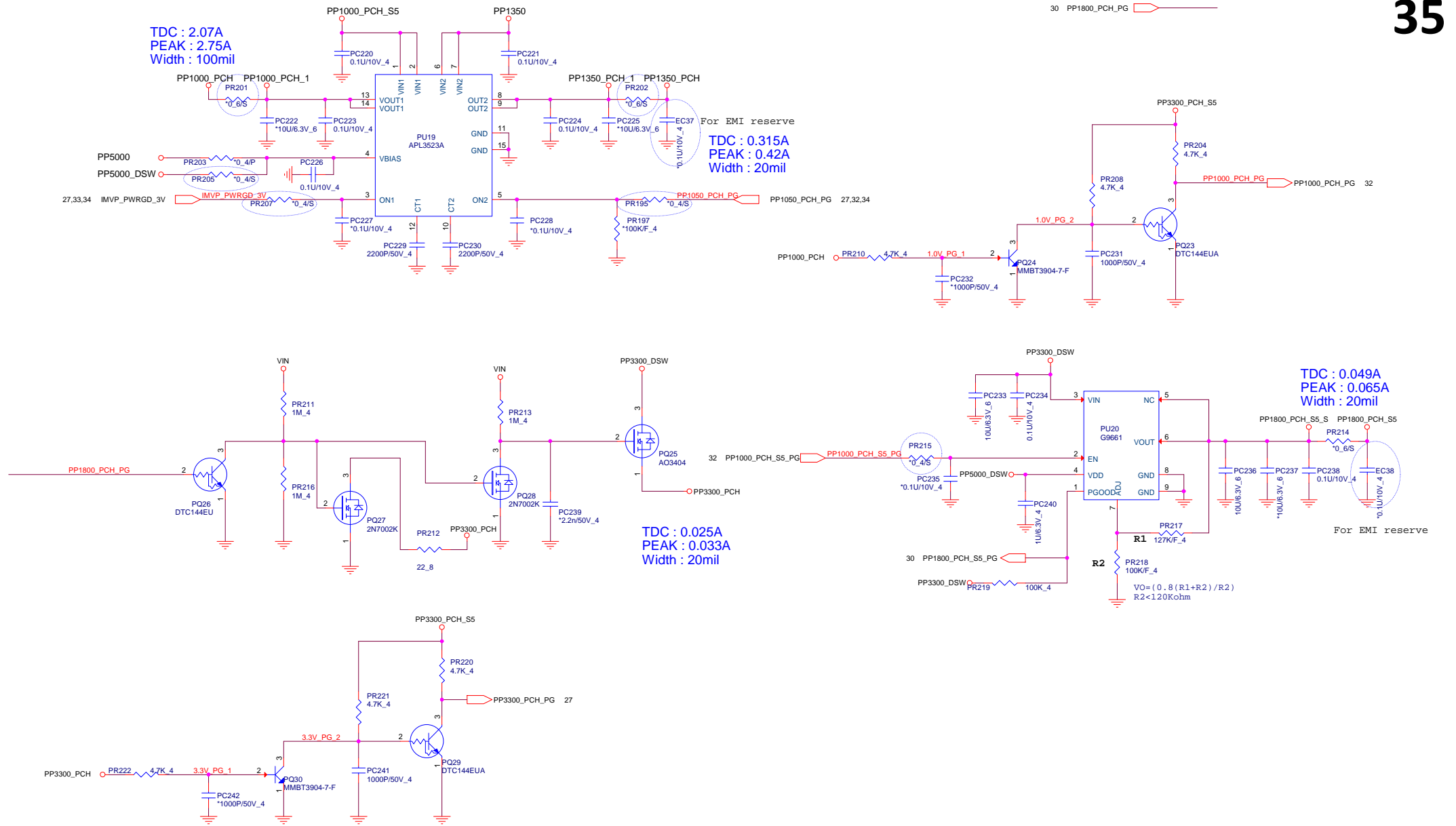


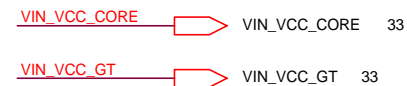
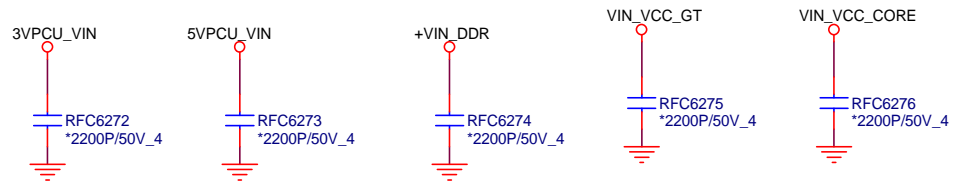




31 VSFR_EN 
 27,32 SUSP_VR_EN 
 27,32,35 PP1050_PCH_PG 



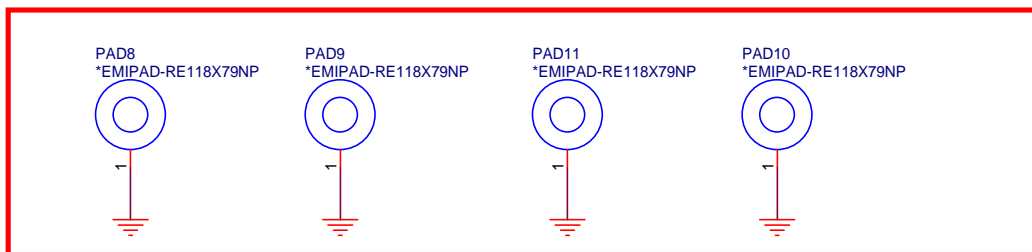




TOP

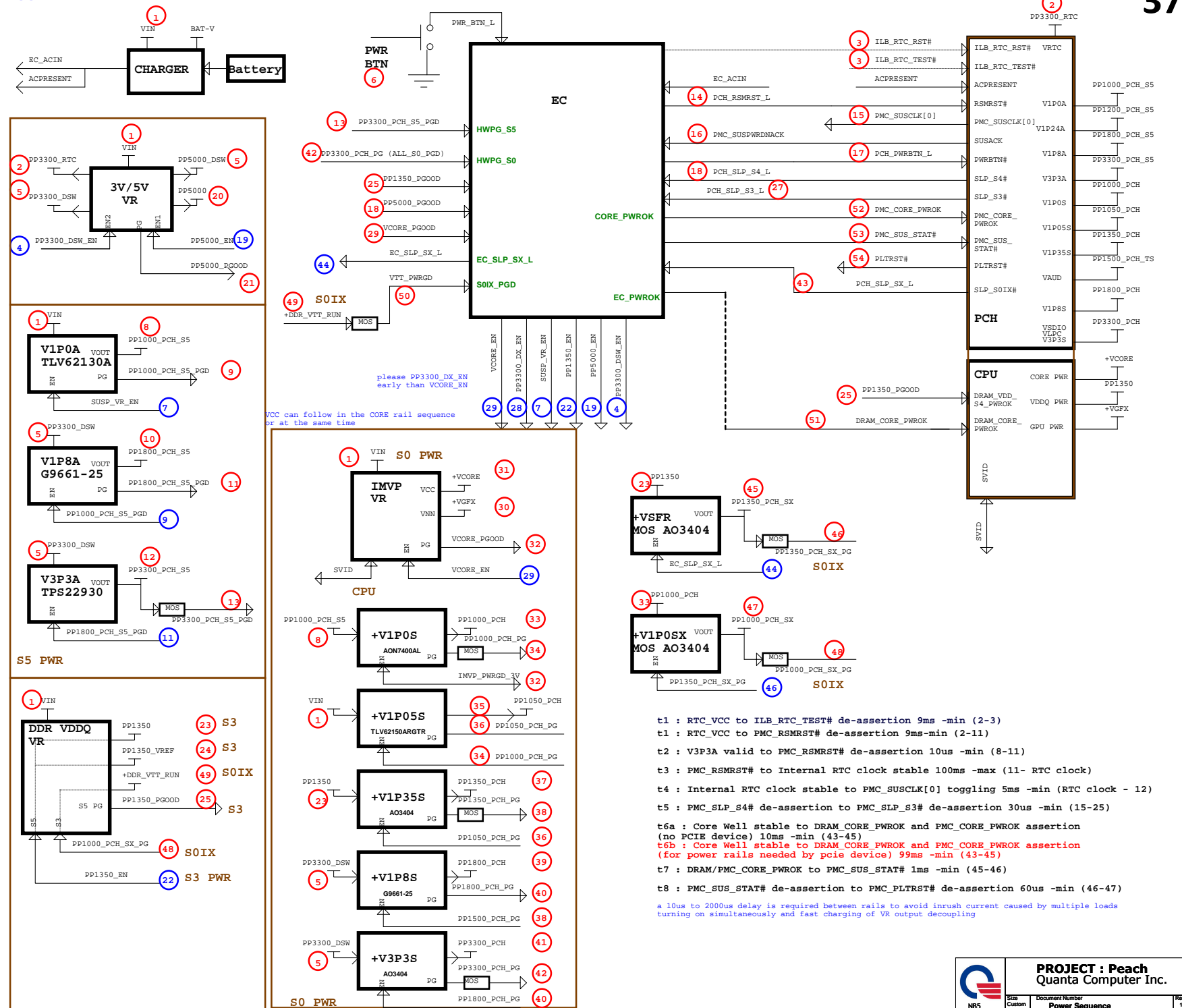


BOTTOM



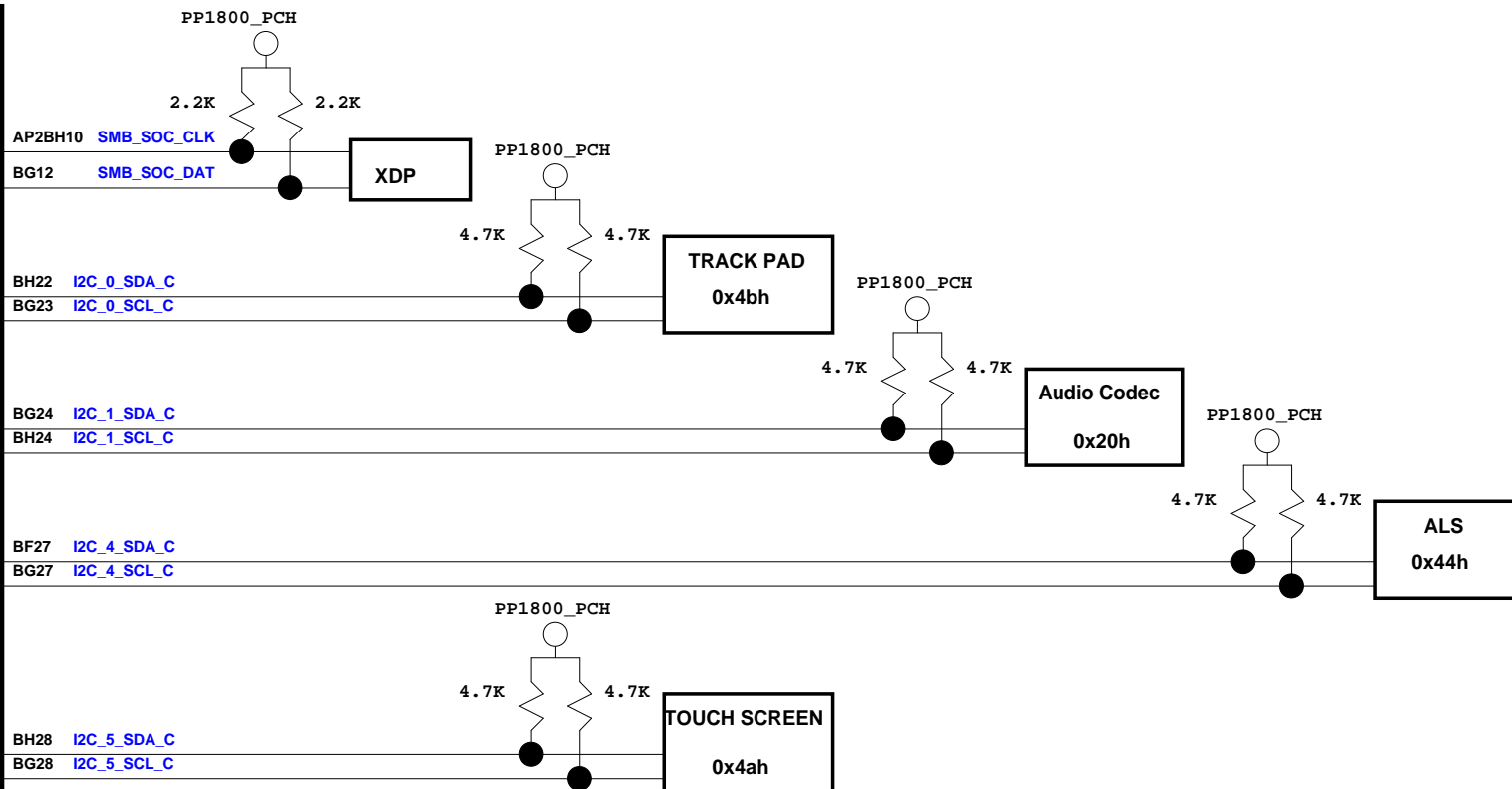
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SMBUS
Bay-trail M

I2C



KBC
TI
SMBUS

